

FORM PTO-1449

ATTY. DOCKET NO.  
01827.0037.CPUS01SERIAL NO.  
09/887,877LIST OF PATENTS AND OTHER ITEMS FOR APPLICANT'S  
INFORMATION DISCLOSURE STATEMENT

## APPLICANT:

Donald B. Eidson, et al.

## FILING DATE:


June 21, 2001

## GROUP:

2631 2133

MAR 05 2002 (Use several sheets if necessary)

## U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE
	6,023,783	02/08/00	Divsalar et al.	714	792	05/15/97

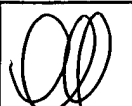
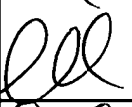
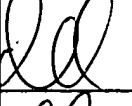

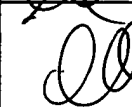
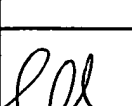
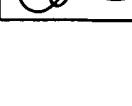
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## FOREIGN PATENT DOCUMENTS

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			None			

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## OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)

	Benedetto, S. et al., <i>A Soft-Input Soft-Output Maximum A Posteriori (MAP) Module to Decode Parallel and Serial Concatenated Codes</i> , TDA Progress Report 42-127, November 15, 1996, pages 1-20.
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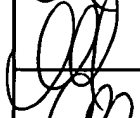
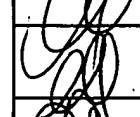
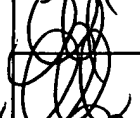
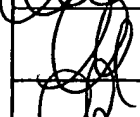
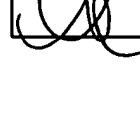


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	Divsalar, D. et al., <i>Turbo Trellis Coded Modulation with Iterative Decoding for Mobile Satellite Communications</i> , Jet Propulsion Laboratory, California Institute of Technology, 4800 Oak Grove, Dr., Pasadena, CA 91109, (7 pages).
	Divsalar et al., <i>Serial and Hybrid Concatenated Codes with Applications</i> ; Jet Propulsion Laboratory, California Institute of Technology, Pasadena, CA; <i>International Symposium on Turbo Codes, Brest, France, 1997</i> . Pages 80-87
	Divsalar, D., et al., <i>Serial Turbo Trellis Coded Modulation with Rate-1 Inner Code</i> ; IEEE, June 2000, page 194.
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	Hoehner, Peter et al., "Turbo DPSK": <i>Iterative Differential PSK Demodulation and Channel Decoding</i> , IEEE Transactions on Communications, Vol. 47, No. 6, June, 1999, (15 pages).
	Narayanan, Krishna R., et al., <i>A Serial Concatenation Approach to Iterative Demodulation and Decoding</i> , IEEE Transactions on Communications, Vol., 47, No. 7, July, 1999, pages 956 -
	Peleg, M. et al., <i>On Interleaved, Differentially Encoded Convolutional Codes</i> ; IEEE Transactions on information Theory, November 1999, pages 2572-2582.
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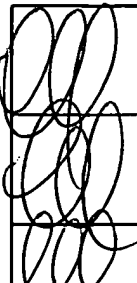
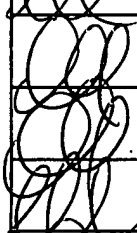
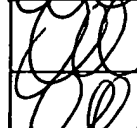

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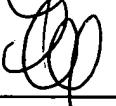

## U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE
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## FOREIGN PATENT DOCUMENTS

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			None			

## OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)

	A1	Benedetto, S. and Divsalar, D., "Turbo Codes: Analysis, Design, Iterative Decoding and Applications," Course 909, Int'l Courses for Telecom and Semiconductor Professionals, Jet Propulsion Laboratory, October 25-29, 1999.
	A2	Benedetto, S. and Divsalar, D., "Turbo Codes: Analysis, Design, Iterative Decoding and Applications," Course 909, Int'l Courses for Telecom and Semiconductor Professionals PART I, Jet Propulsion Laboratory, October 25-29, 1999.
	A3	Benedetto, S. and Divsalar, D., "Turbo Codes: Analysis, Design, Iterative Decoding and Applications," Course 909, Int'l Courses for Telecom and Semiconductor Professionals PART II, Jet Propulsion Laboratory, October 25-29, 1999.

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ATTY. DO/ NO.

01827.0037.00US00

SERIAL NO.

09/602,690

APPLICANT:

Donald B. Eidson, et al.

FILING DATE:

June 23, 2000

GROUP:

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09/887877

06/21/01


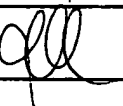
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	AA	Benedetto, S. and Divsalar, D., <i>Serial Concatenated Trellis Coded Modulation with Rate-1 Inner Code</i> ; International Courses for Telecom and Semiconductor Professionals, Course 909 Turbo Codes: Analysis, Design, Iterative Decoding and Applications, October 1999, pages 324-339.
	AB	Divsalar, D., et al., <i>Serial Turbo Trellis Coded Modulation with Rate-1 Inner Code</i> ; IEEE, June 2000, page 194.

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3/1/03



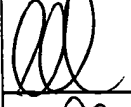
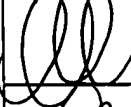



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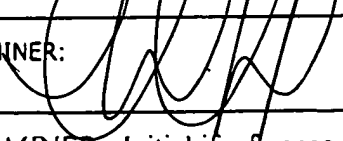
<b>FORM PTO-1449</b>  <b>LIST OF PATENTS AND OTHER ITEMS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT</b>  (Use several sheets if necessary)	<b>ATTY. DOR</b> 01827.003 / JSC0	<b>SERIAL NO.</b> 09/602,690
	<b>APPLICANT:</b> Donald B. Eidson, et al.	
	<b>FILING DATE:</b> June 23, 2000	<b>GROUP:</b> 2734 2133

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	Benedetto, S. et al., <i>A Soft-Input Soft-Output Maximum A Posteriori (MAP) Module to Decode Parallel and Serial Concatenated Codes</i> , TDA Progress Report 42-127, November 15, 1996, pages 1-20.
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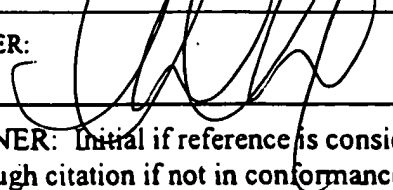
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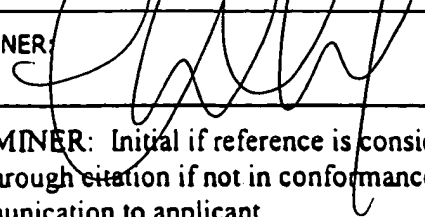
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